

Developer Kit for OpenVINO™ Toolkit

tdk.terasic.com

Quick Start Guide



FPGA Board Power Switch –
FPGA Board 12V DC Jack –
FPGA USB Blaster Port –





PCIe Slot to M2 Socket Adapter



¹ What's in the box?



- Terasic Developer Kit for OpenVINO™ Toolkit (TDK)
- USB 3.0 Cable for PCle Conversion (Installed)
- 03 Mini USB Cable (installed)
- 04 12V Power Supply (for FPGA Board)
- 05 5V Power Supply (for PC system)

² Power On Test

- Ol Connect a keyboard, a mouse, and a HDMI monitor to the PC system.
- Make sure the Mini USB Cable is still securely connected to the Developer Kit for OpenVINO™ Toolkit and the PC system for the FPGA programming.



Make sure the USB 3.0 Cable is also securely connected to the Developer Kit for OpenVINO™ Toolkit and the PC system for PCIe link.



O4 Connect the DC 12V power to the Developer Kit for OpenVINO™ Toolkit (FPGA System).





Dower on the Developer Kit for OpenVINO™ Toolkit



- Onnect the DC 5V power to the PC, then the PC wil be powered on directly.
 - Note: Please do NOT connect DC 12V power to the PC system or it will be damaged permanently.





- After the Linux Desktop is displayed on the monitor, double click the batch file "Power On Test.sh" on the desktop.
- When the message "Do you want to install..." appears on the Terminal, enter "y", then press ENTER key on the keyboard.

79 The car license number recognition demo will be executed, as shown in the picture below, to indicate the Power On Test is successful.



³ Contents of Board System CD

Users can download the System CD from the link below: http://tdk.terasic.com/cd



For further discussion, support, and resources, please go to: http://tdk.terasic.com

If you encounter any problem, please contact us below:

⊠Email: support@terasic.com

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